

2026 IEEE 18th International Conference on Solid-State and Integrated Circuit Technology (ICSICT)

📅 Oct. 27 - 30, 2026 📍 Hangzhou, China

ABOUT CONFERENCE

2026 IEEE 18th International Conference on Solid-State and Integrated Circuit Technology (ICSICT) in the series aiming to provide an international forum for the presentation and discussion of recent advances in solid-state and integrated circuit technology. **The conference will be held on Oct. 27 - 30, 2026 in Hangzhou, China.**

All aspects of solid-state digital IC & system level design, analog, devices, process technologies and other related research are within the scope of the conference. The ICSICT 2026 Organizing Committee invites you to submit your papers and looks forward to seeing you in Hangzhou!

Track 8: Power Devices & Power IC

Track Chairs



Long Zhang

Chair

Southeast University, China



Jiafei Yao

Co-chair

Nanjing University of Posts and Telecommunications, China



Zekun Zhou

Co-chair

University of Electronic Science and Technology of China, China

Topics

- ➡ Silicon-based Power Devices
- ➡ Wide Bandgap Power Devices
- ➡ Power Integrated Circuits, Modules & Systems
- ➡ Power Integrating & Packaging Technology
- ➡ Power Device Reliability
- ➡ Power Device Modeling & Simulation

Publication

Submissions to ICSICT 2026 will be peer reviewed on the basis of technical quality, relevance to conference topics, originality, significance, clarity, etc.

Accepted papers will be submitted for inclusion into IEEE Xplore subject to meeting IEEE Xplore's scope and quality requirements. The proceedings will be submitted for indexing by EI Compendex and Scopus.

Paper Submission

Authors are recommended to submit a 3-page paper; the minimum length is 3 pages and the maximum is 10 double-column pages (including references, tables, and figures).

* View the full submission guidelines:

<https://www.icsict.org/Submission.html>

* Submit your paper through the following link or QR code:

<https://easychair.org/conferences/?conf=icsict2026>



Important Dates

Full-Length Paper Submission Deadline July 16, 2026

Notification of Acceptance August 16, 2026

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College of Integrated Circuits
ZHEJIANG UNIVERSITY



电子科技大学
长三角研究院(湖州)

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